

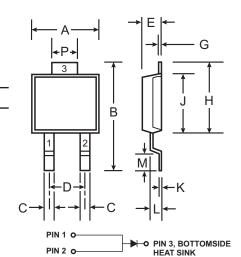
3A HIGH VOLTAGE SCHOTTKY BARRIER RECTIFIER **POWERMITE®3**

NOT RECOMMENDED FOR NEW DESIGNS. **Features** - USE PDS3100

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Reverse Breakdown Voltage
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- Lead Free Finish, RoHS Compliant Version (Note 2)

Mechanical Data

- Case: POWERMITE®3
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish). (e3)
- Polarity: See Diagram
- Marking: See Page 3
- Ordering Information: See Page 3
- Weight: 0.072 grams (approximate)



Dim Min Max 4.03 4.09 Α В 6.40 6.61 С .864 .914 D 1.83 NOM 1.10 Ε 1.14 G .173 .203 н 5.01 5.17 J 4.37 4.43 Κ .173 .203 L .71 .77 .46 М 36 Р 1.73 1.83 All Dimensions in mm

POWERMITE®3

Pins 1 & 2 must be electrically Note: connected at the printed circuit board.

Maximum Ratings @ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load, For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	100	V
RMS Reverse Voltage	V _{R(RMS)}	70	V
Average Rectified Output Current (Also see Figure 5)	I _O	3	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load @ T _C = 90°C	I _{FSM}	50	А
Typical Thermal Resistance Junction to Soldering Point	Reus	3.5	°C/W
Typical Thermal Resistance Junction to Case	ReJC	1.6	°C/W
Operating Temperature Range	Tj	-55 to +125	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

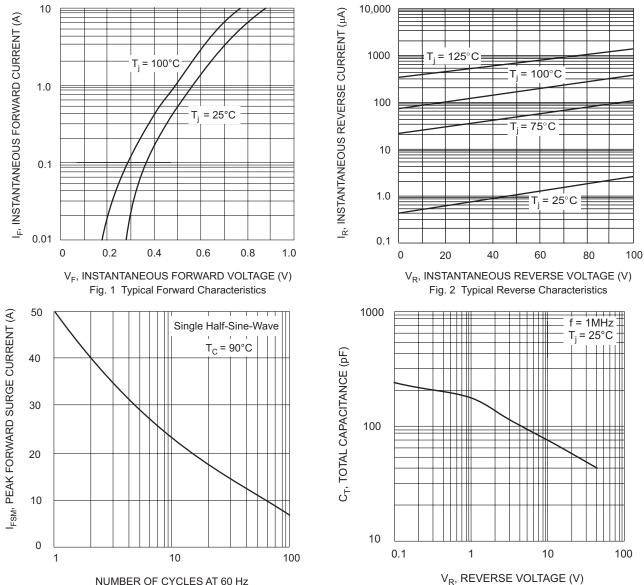
Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	100	_	_	V	I _R = 0.2mA
Forward Voltage	V _F		0.72 0.60 0.80 0.69	0.76 — — —	V	$\begin{array}{l} I_F = 3A, \ T_j = 25^{\circ}C \\ I_F = 3A, \ T_j = 100^{\circ}C \\ I_F = 6A, \ T_j = 25^{\circ}C \\ I_F = 6A, \ T_j = 100^{\circ}C \end{array}$
Reverse Current (Note 1)	I _R	_	3 0.35	100 20	μA mA	$T_j = 25$ °C, $V_R = 100$ V $T_j = 100$ °C, $V_R = 100$ V
Total Capacitance	Ст	_	100	_	pF	f = 1.0MHz, V _R = 4.0V DC

Notes: 1. Short duration test pulse used to minimize self-heating effect.

2. RoHS revision 13.2.2003. High Temperature Solder Exemption Applied see EU Directive Annex Note 7.



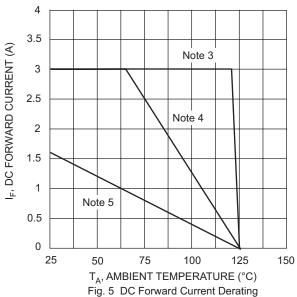


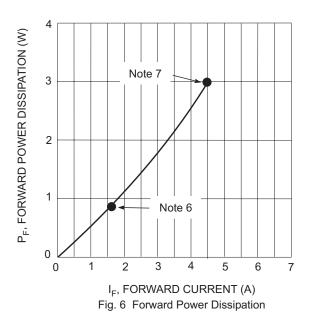
NUMBER OF CYCLES AT 60 Hz Fig. 3 Max Non-Repetitive Peak Forward Surge Current

Fig. 4 Typical Total Capacitance vs. Reverse Voltage

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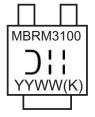
Ordering Information (Note 8)

Device	Packaging	Shipping
MBRM3100-13-F	POWERMITE®3	5000/Tape & Reel

Notes: 3. Ta = Tsoldering point, $R_{\theta JS} = 3.5^{\circ} \text{C/W}, \ R_{\theta SA} = 0^{\circ} \text{C/W}.$

- Device mounted on GETEK substrate, 2"x2", 2 oz. copper, double-sided, cathode pad dimensions 0.75" x 1.0", anode pad dimensions 0.25" x 1.0". R_{θ,JA} in range of 30-35°C/W.
- Device mounted on FR-4 substrate, 2"x2", 2 oz. copper, single-sided, pad layout as per Diodes Inc. suggested pad layout document AP02001 which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf. R_{θJA} in range of 115-125°C/W.
- 6. Maximum power dissipation when the device is mounted in accordance to the conditions described in Note 4.
- 7. Maximum power dissipation when the device is mounted in accordance to the conditions described in Note 3.
- 8. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



MBRM3100 = Product type marking code

O!! = Manufacturers' code marking

YYWW = Date code marking

YY = Last digit of year ex: 02 for 2002

WW = Week code 01 to 52

(K) = Factory Designator

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