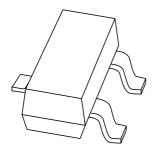
## **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# BCV26; BCV46 PNP Darlington transistors

Product specification Supersedes data of 1999 Apr 08 2004 Jan 13





**Philips Semiconductors** 

# **PNP Darlington transistors**

## **BCV26**; **BCV46**

#### **FEATURES**

- High current (max. 500 mA)
- Low voltage (max. 60 V)
- Very high DC current gain (min. 10000).

#### **APPLICATIONS**

• Where very high amplification is required.

#### **DESCRIPTION**

PNP Darlington transistor in a SOT23 plastic package. NPN complements: BCV27 and BCV47.

#### **MARKING**

TYPE NUMBER	MARKING CODE <sup>(1)</sup>
BCV26	FD*
BCV46	FE*

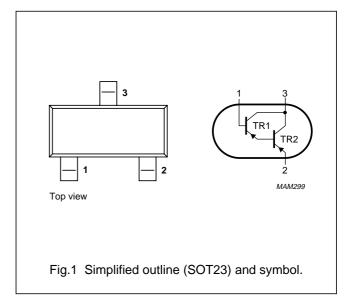
#### Note

\* = p : Made in Hong Kong.
 \* = t : Made in Malaysia.

\* = W : Made in China.

#### **PINNING**

PIN	DESCRIPTION
1	base
2	emitter
3	collector



#### **ORDERING INFORMATION**

TYPE	PACKAGE			
NUMBER	NAME DESCRIPTION VERS			
BCV26	_	plastic surface mounted package; 3 leads	SOT23	
BCV46				

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# PNP Darlington transistors

BCV26; BCV46

#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	BCV26		_	-40	V
	BCV46		_	-80	V
V <sub>CES</sub>	collector-emitter voltage	V <sub>BE</sub> = 0			
	BCV26		_	-30	V
	BCV46		_	-60	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	-10	V
I <sub>C</sub>	collector current (DC)		_	-500	mA
I <sub>CM</sub>	peak collector current		_	-800	mA
I <sub>B</sub>	base current (DC)		_	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

<sup>1.</sup> Transistor mounted on an FR4 printed-circuit board.

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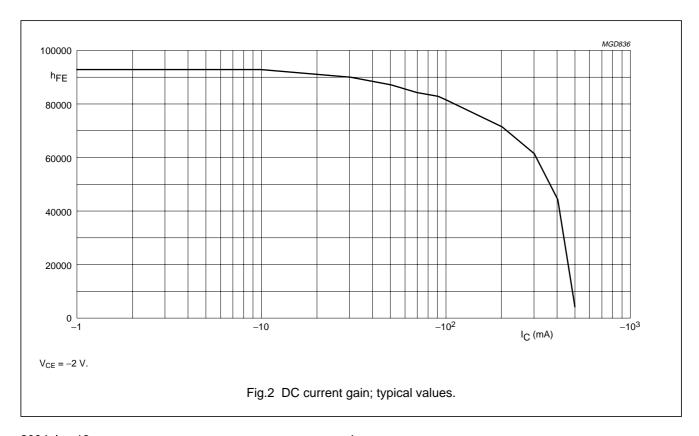
# PNP Darlington transistors

BCV26; BCV46

#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current					
	BCV26	$I_E = 0$ ; $V_{CB} = -30 \text{ V}$	_	_	-100	nA
	BCV46	$I_E = 0; V_{CB} = -60 \text{ V}$	_	_	-100	nA
I <sub>EBO</sub>	emitter cut-off current	$I_C = 0; V_{EB} = -10 \text{ V}$	_	_	-100	nA
h <sub>FE</sub>	DC current gain	$I_C = -1 \text{ mA}; V_{CE} = -5 \text{ V}; \text{ (see Fig.2)}$				
	BCV26		4000	_	_	
	BCV46		2000	_	_	
	DC current gain	$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}; \text{ (see Fig.2)}$				
	BCV26		10000	_	_	
	BCV46		4000	_	_	
	DC current gain	$I_C = -100 \text{ mA}; V_{CE} = -5 \text{ V}; \text{ (see Fig.2)}$				
	BCV26		20000	_	_	
	BCV46		10000	_	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = -100 \text{ mA}; I_B = -0.1 \text{ mA}$	_	_	-1	V
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = -100 \text{ mA}; I_B = -0.1 \text{ mA}$	_	_	-1.5	V
V <sub>BEon</sub>	base-emitter on-state voltage	$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}$	_	_	-1.4	V
f <sub>T</sub>	transition frequency	$I_C = -30 \text{ mA}; V_{CE} = -5 \text{ V}; f = 100 \text{ MHz}$	_	220	_	MHz



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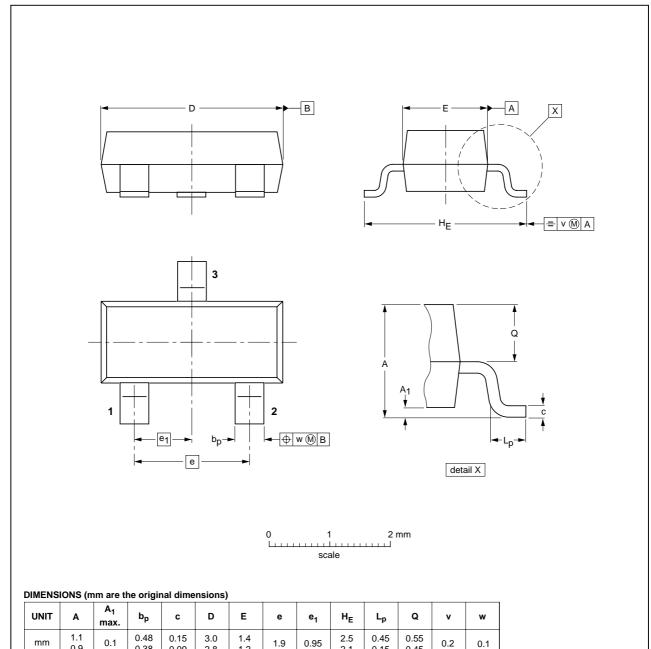
# PNP Darlington transistors

BCV26; BCV46

#### **PACKAGE OUTLINE**

#### Plastic surface mounted package; 3 leads

SOT23



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION ISSUE DATE	
SOT23		TO-236AB				<del>-97-02-28</del> 99-09-13

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Philips Semiconductors Product specification

#### PNP Darlington transistors

BCV26; BCV46

#### **DATA SHEET STATUS**

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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#### **Contact information**

For additional information please visit http://www.semiconductors.philips.com. Fax: +31 40 27 24825 For sales offices addresses send e-mail to: sales.addresses@www.semiconductors.philips.com.

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