

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

## TC74AC14P, TC74AC14F, TC74AC14FN, TC74AC14FT

### Hex Schmitt Inverter

The TC74AC14 is an advanced high speed CMOS SCHMITT INVERTER fabricated with silicon gate and double-layer metal wiring C<sup>2</sup>MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

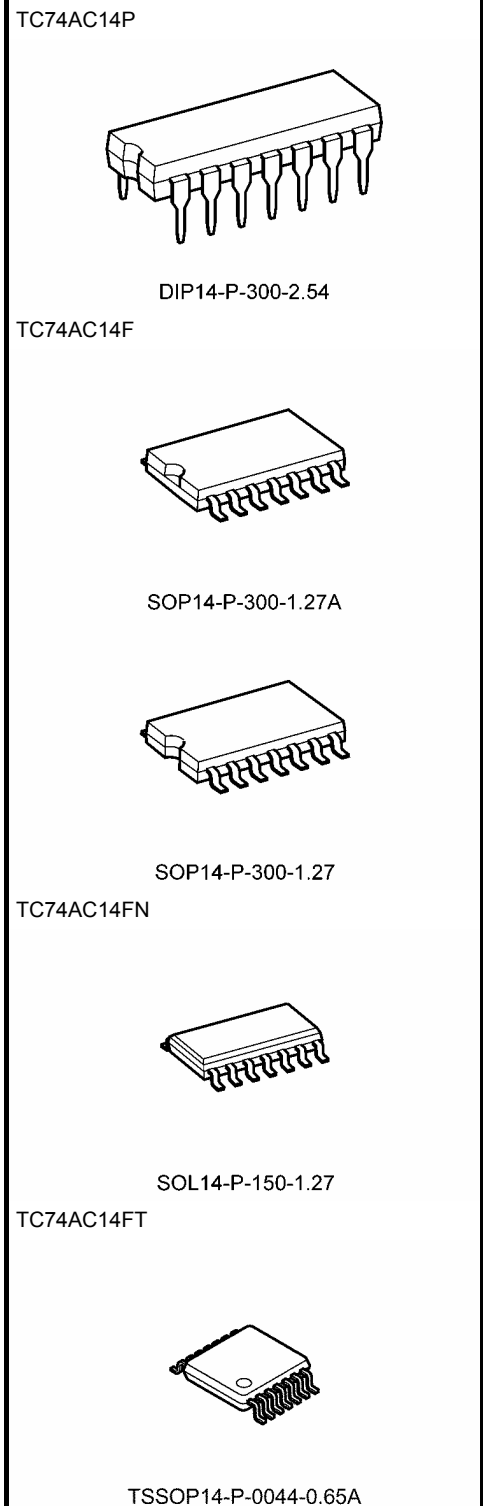
Pin configuration and function are the same as the TC74AC04 but the inputs have hysteresis and with its schmitt trigger function, the TC74AC14 can be used as a line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

### Features

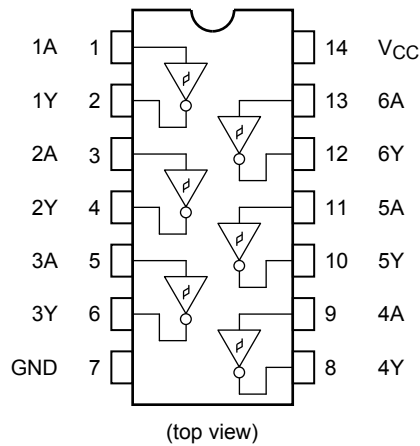
- High speed:  $t_{pd} = 5.3 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \mu\text{A (max)}$  at  $T_a = 25^\circ\text{C}$
- Symmetrical output impedance:  $|I_{OH}| = I_{OL} = 24 \text{ mA (min)}$   
 Capability of driving  $50 \Omega$  transmission lines.
- Balanced propagation delays:  $t_{pLH} \approx t_{pHL}$
- Wide operating voltage range:  $V_{CC (opr)} = 2 \text{ to } 5.5 \text{ V}$
- Pin and function compatible with 74F14

Note: xxxFN (JEDEC SOP) is not available in Japan.

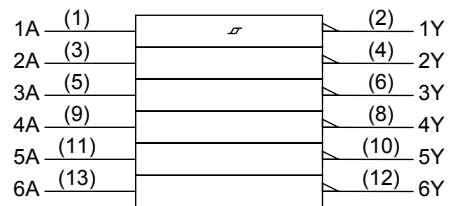


Weight	
DIP14-P-300-2.54	: 0.96 g (typ.)
SOP14-P-300-1.27A	: 0.18 g (typ.)
SOP14-P-300-1.27	: 0.18 g (typ.)
SOL14-P-150-1.27	: 0.12 g (typ.)
TSSOP14-P-0044-0.65A	: 0.06 g (typ.)

## Pin Assignment



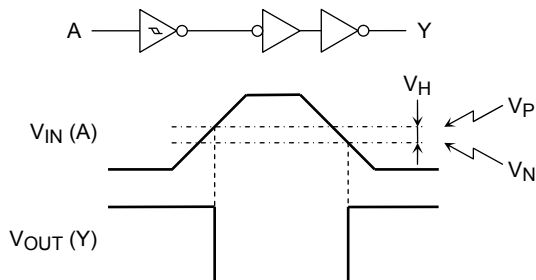
## IEC Logic Symbol



## Truth Table

A	Y
L	H
H	L

## System Diagram, Waveform



## Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	$V_{CC}$	-0.5 to 7.0	V
DC input voltage	$V_{IN}$	-0.5 to $V_{CC} + 0.5$	V
DC output voltage	$V_{OUT}$	-0.5 to $V_{CC} + 0.5$	V
Input diode current	$I_{IK}$	$\pm 20$	mA
Output diode current	$I_{OK}$	$\pm 50$	mA
DC output current	$I_{OUT}$	$\pm 50$	mA
DC $V_{CC}$ /ground current	$I_{CC}$	$\pm 150$	mA
Power dissipation	$P_D$	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	$T_{stg}$	-65 to 150	$^{\circ}C$

Note1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note2: 500 mW in the range of  $T_a = -40$  to  $65^{\circ}C$ . From  $T_a = 65$  to  $85^{\circ}C$  a derating factor of  $-10$  mW/ $^{\circ}C$  should be applied up to 300 mW.

## Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	2.0 to 5.5	V
Input voltage	$V_{IN}$	0 to $V_{CC}$	V
Output voltage	$V_{OUT}$	0 to $V_{CC}$	V
Operating temperature	$T_{opr}$	-40 to 85	°C

Note: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

## Electrical Characteristics

### DC Characteristics

Characteristics	Symbol	Test Condition	$V_{CC}$ (V)	$T_a = 25^\circ\text{C}$			$T_a = -40 \text{ to } 85^\circ\text{C}$		Unit	
				Min	Typ.	Max	Min	Max		
Positive threshold voltage	$V_P$	—	3.0	—	—	2.2	—	2.2	V	
			4.5	—	—	3.2	—	3.2		
			5.5	—	—	3.9	—	3.9		
Negative threshold voltage	$V_N$	—	3.0	0.5	—	—	0.5	—	V	
			4.5	0.9	—	—	0.9	—		
			5.5	1.1	—	—	1.1	—		
Hysteresis voltage	$V_H$	—	3.0	0.3	—	1.2	0.3	1.2	V	
			4.5	0.4	—	1.4	0.4	1.4		
			5.5	0.5	—	1.6	0.5	1.6		
High-level output voltage	$V_{OH}$	$V_{IN} = V_{IL}$	$I_{OH} = -50 \mu\text{A}$	2.0	1.9	2.0	—	1.9	—	V
				3.0	2.9	3.0	—	2.9	—	
				4.5	4.4	4.5	—	4.4	—	
			$I_{OH} = -4 \text{ mA}$	3.0	2.58	—	—	2.48	—	
				4.5	3.94	—	—	3.80	—	
$I_{OH} = -24 \text{ mA}$	3.0	—	—	—	—	—				
	4.5	—	—	—	—	—				
$I_{OH} = -75 \text{ mA (Note)}$	3.0	—	—	—	—	—				
	4.5	—	—	—	—	—				
Low-level output voltage	$V_{OL}$	$V_{IN} = V_{IH}$	$I_{OL} = 50 \mu\text{A}$	2.0	—	0.0	0.1	—	0.1	V
				3.0	—	0.0	0.1	—	0.1	
				4.5	—	0.0	0.1	—	0.1	
			$I_{OL} = 12 \text{ mA}$	3.0	—	—	0.36	—	0.44	
				4.5	—	—	0.36	—	0.44	
$I_{OL} = 24 \text{ mA}$	3.0	—	—	—	—	—				
	4.5	—	—	—	—	—				
$I_{OL} = 75 \text{ mA (Note)}$	3.0	—	—	—	—	—				
	4.5	—	—	—	—	—				
Input leakage current	$I_{IN}$	$V_{IN} = V_{CC}$ or GND	5.5	—	—	$\pm 0.1$	—	$\pm 1.0$	$\mu\text{A}$	
Quiescent supply current	$I_{CC}$	$V_{IN} = V_{CC}$ or GND	5.5	—	—	4.0	—	40.0	$\mu\text{A}$	

Note: This spec indicates the capability of driving 50  $\Omega$  transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

## AC Characteristics ( $C_L = 50 \text{ pF}$ , $R_L = 500 \text{ }\Omega$ , input: $t_r = t_f = 3 \text{ ns}$ )

Characteristics	Symbol	Test Condition	Ta = 25°C			Ta = -40 to 85°C		Unit	
			V <sub>CC</sub> (V)	Min	Typ.	Max	Min		Max
Propagation delay time	t <sub>pLH</sub>	—	3.3 ± 0.3	—	8.1	13.2	1.0	15.0	ns
	t <sub>pHL</sub>		5.0 ± 0.5	—	6.0	9.7	1.0	11.0	
Input capacitance	C <sub>IN</sub>	—	—	5	10	—	10	pF	
Power dissipation capacitance	C <sub>PD</sub>	(Note)	—	29	—	—	—	pF	

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6 \text{ (per gate)}$$

## Package Dimensions

DIP14-P-300-2.54

Unit : mm

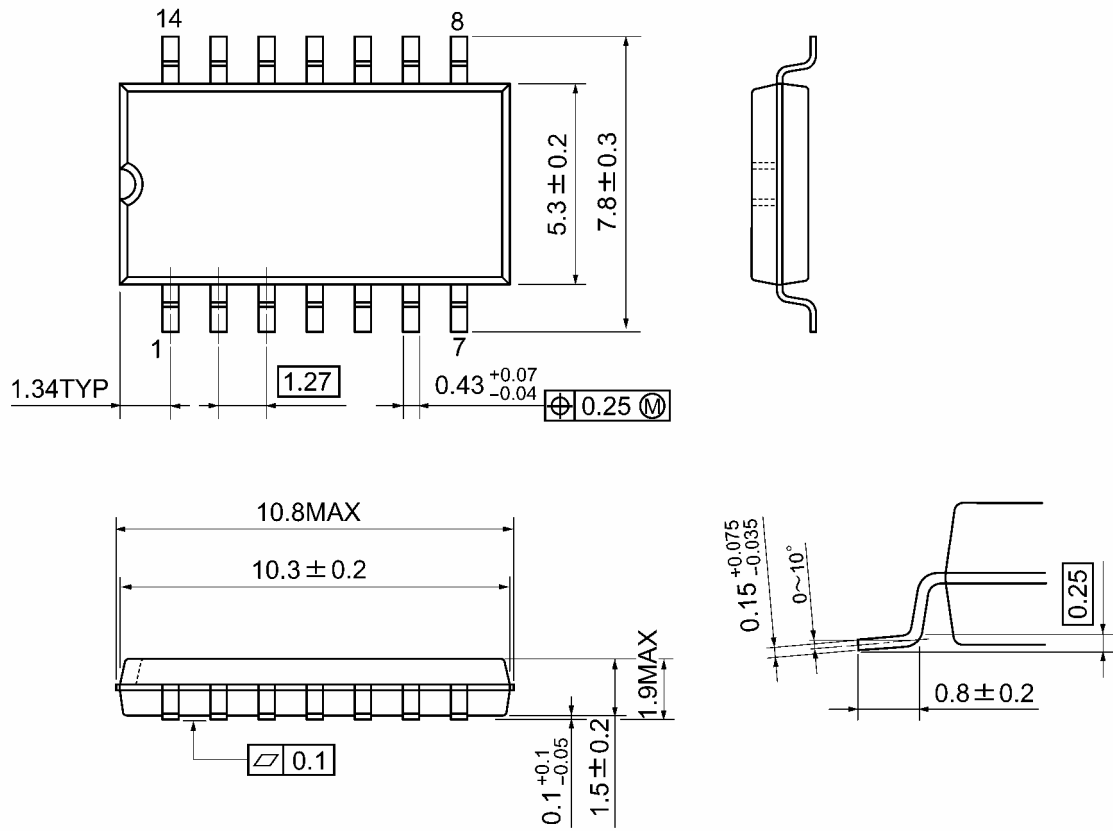


Weight: 0.96 g (typ.)

**Package Dimensions**

SOP14-P-300-1.27A

Unit: mm

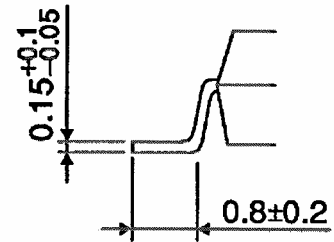
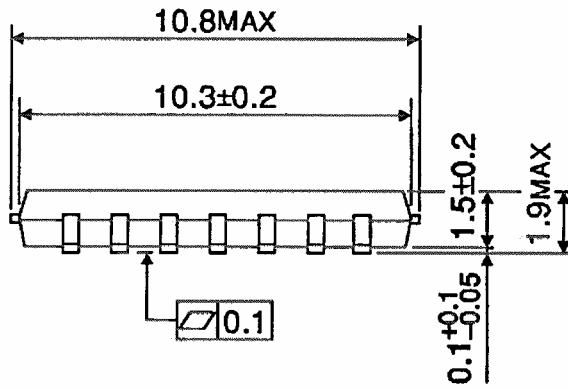
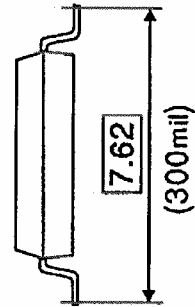
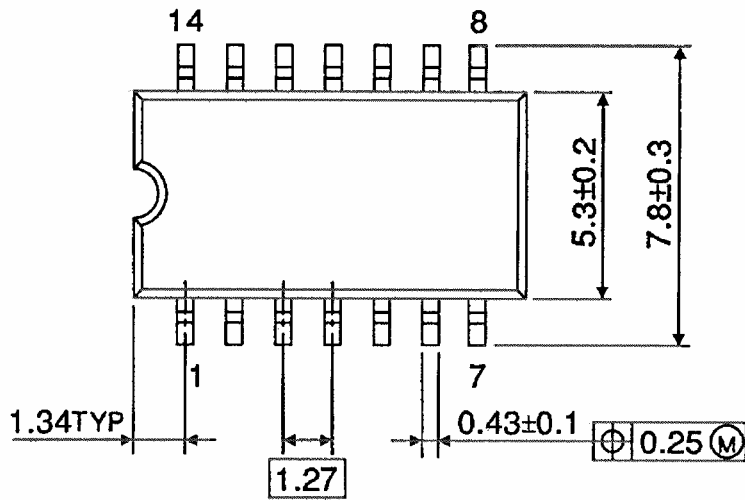


Weight: 0.18 g (typ.)

**Package Dimensions**

SOP14-P-300-1.27

Unit : mm

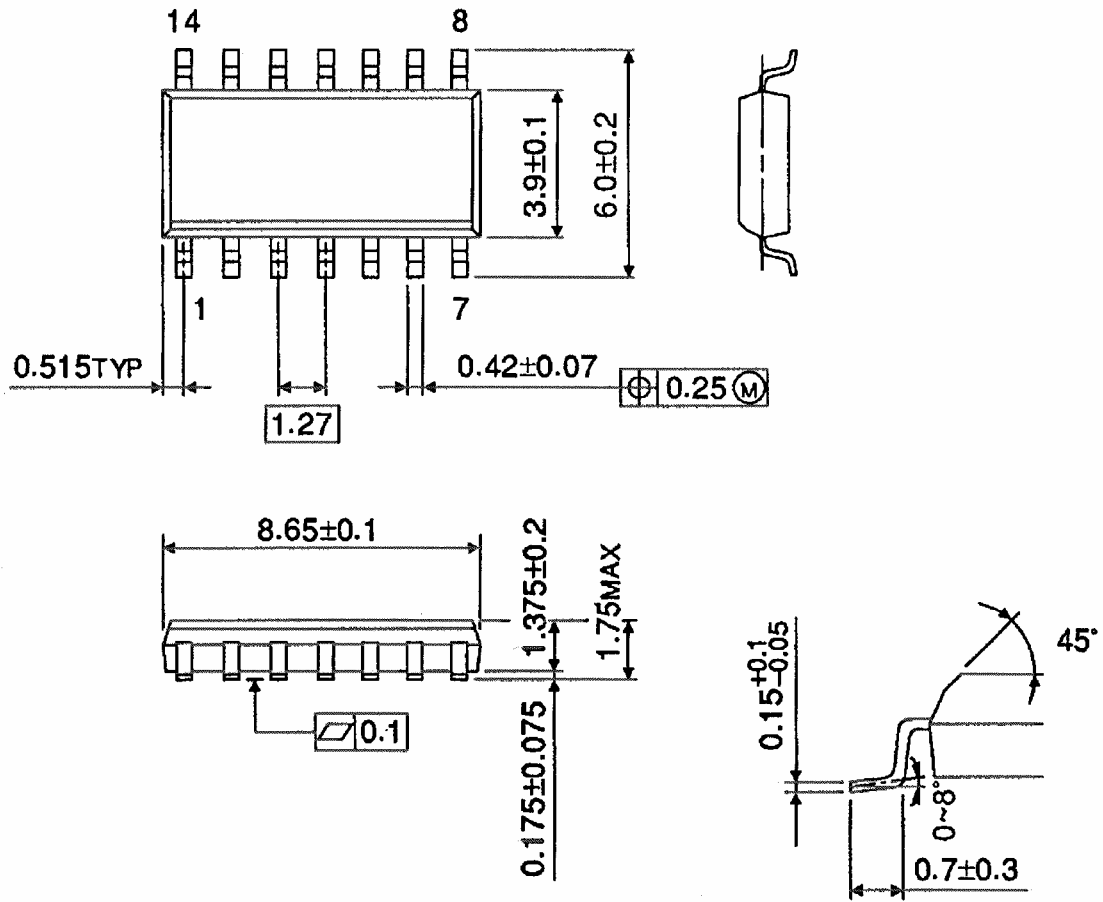


Weight: 0.18 g (typ.)

Package Dimensions (Note)

SOL14-P-150-1.27

Unit : mm



Note: This package is not available in Japan.

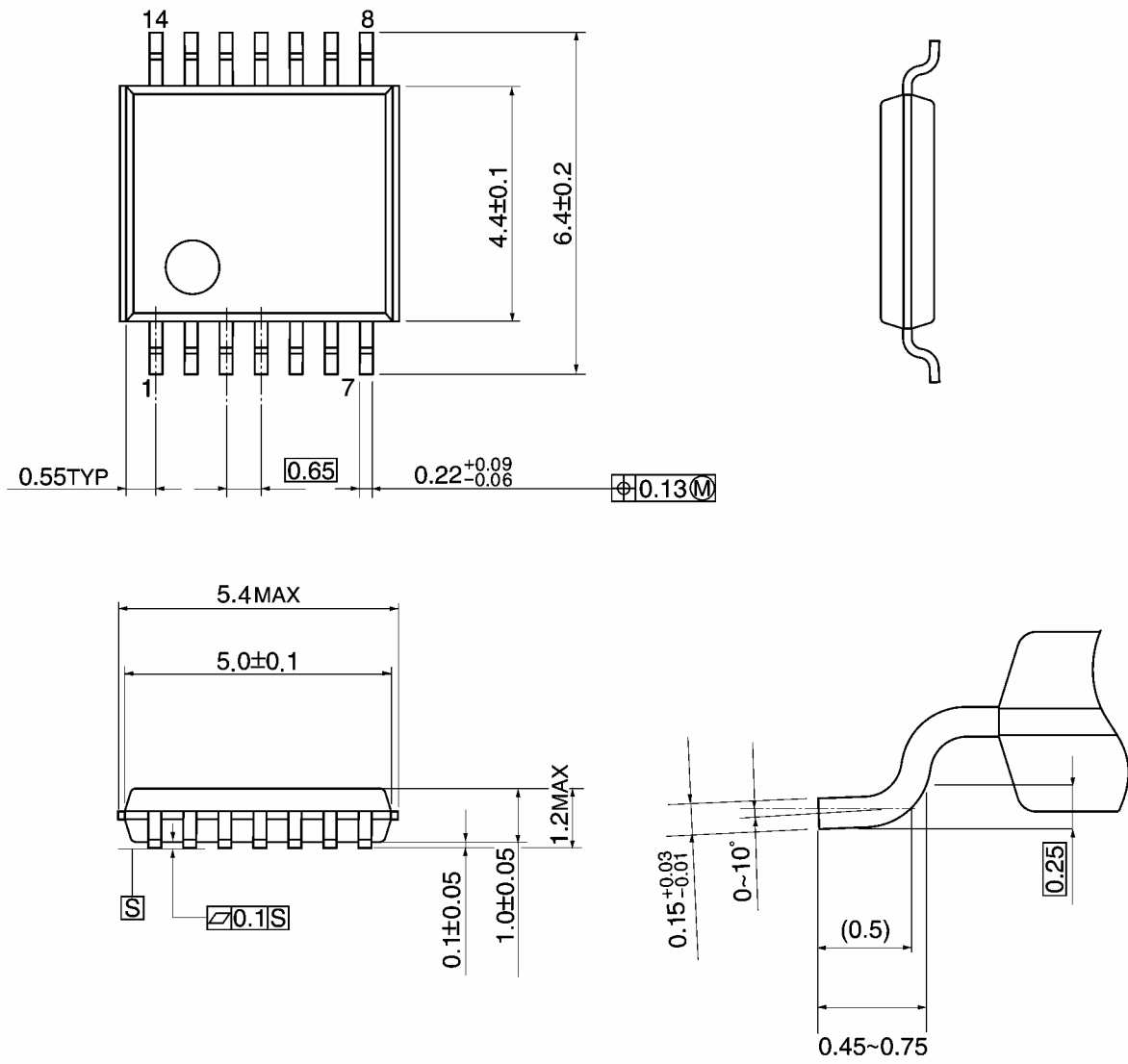
Weight: 0.12 g (typ.)



**Package Dimensions**

TSSOP14-P-0044-0.65A

Unit: mm



Weight: 0.06 g (typ.)

**Note: Lead (Pb)-Free Packages****DIP14-P-300-2.54 SOP14-P-300-1.27A SOL14-P-150-1.27 TSSOP14-P-0044-0.65A****RESTRICTIONS ON PRODUCT USE**

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