General purpose (dual digital transistors) UMH11N/IMH11A

Features

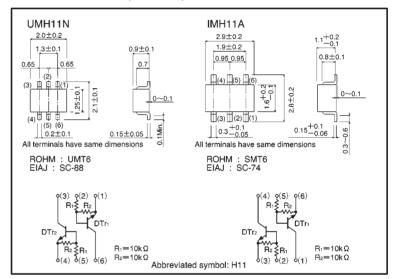
- Two DTC114E chips in a UMT or SMT package.
- Mounting possible with UMT3 or SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

Structure

Epitaxial planar type NPN silicon transistor (Built-in resistor type)

The following characteristics apply to both DTr₁ and DTr₂.

External dimensions (Units: mm)



● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
L		Vin	40	V	
Input voltage		VIN	-10		
Output current		lo	50	mA	
Collector current		IC(Max.)	100	mA	
Power dissipation	UMH11N	Pd	150 (TOTAL)	mW	*1
	IMH11A	Pu	300 (TOTAL)	IIIVV	*2
Junction temperature		Tj	150	Ĉ	
Storage temperature		Tstg	-55~ + 150	Ç	

^{*1 120}mW per element must not be exceeded.

(96-490-C114E)



^{*2 200}mW per element must not be exceeded.

Transistors UMH11N / IMH11A

• Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltage	VI (off)	_	_	0.5	V	Vcc=5V, Io=100 μ A	
	VI (on)	3	_	_		Vo=0.3V, lo=10mA	
Output voltage	VO(on)	_	0.1	0.3	٧	lo/li=10mA/0.5mA	
Input current	lı	_	_	0.88	mA	V ₁ =5V	
Output current	IO (off)	_	_	0.5	μA	Vcc=50V, Vi=0V	
DC current gain	Gı	30	_	_	_	Vo=5V, Io=5mA	
Transition frequency	fт	_	250	_	MHz	VcE=10mA, IE=-5mA, f=100MHz *	
Input resistance	R ₁	7	10	13	kΩ	<u> </u>	
Resistance ratio	R2/R1	0.8	1	1.2	_	_	

^{*} Transition frequency of the device

Packaging specifications

	Packaging type	Taping		
	Code	TN	T110	
Part No.	Basic ordering unit (pieces)	3000	3000	
UMH11N		0	_	
IMH11A		_	0	

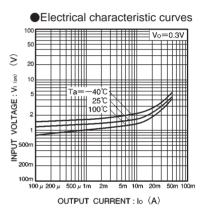


Fig.1 Input voltage vs. output current (ON characteristics)

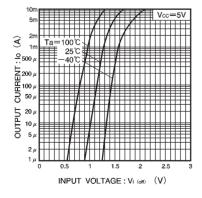


Fig.2 Output current vs. input voltage (OFF characteristics)

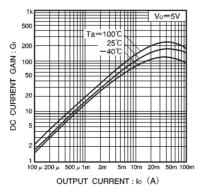


Fig.3 DC current gain vs. output current

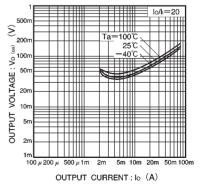


Fig.4 Output voltage vs. output current

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