TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WZ08FU,TC7WZ08FK

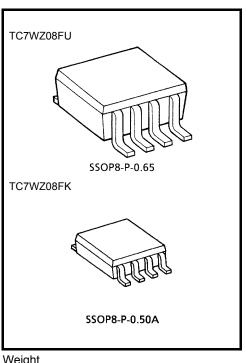
Dual 2 Input AND Gate

Features

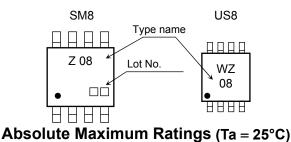
- High output drive: ±24 mA (min) at V_{CC} = 3 V
- Super high speed operation: t_{pd} = 2.5 ns (typ.)

at V_{CC} = 5 V, 50 pF

- Operation voltage range: V_{CC (opr)} = 1.65~5.5 V
- 5.5-V Tolerant inputs
- 5.5-V Power down protection outputs
- Matches the performance of TC74LCX series when operated at 3.3-V V_{CC}



Marking



Characteristics

Weight SSOP8-P-0.65 SSOP8-P-0.50A

Unit

Rating

: 0.02 g (typ.) : 0.01 g (typ.)

Pin Assignment (top view)

1A 1 1B 2	8 V _{CC}
2Y 3	6 2B
GND 4	5 2A

Power supply voltage Vcc -0.5~6 V DC input voltage -0.5~6 ν VIN DC output voltage -0.5~6 V Vout -20 Input diode current Ιικ mΑ Output diode current -20 mΑ lok DC output current ± 50 mΑ lout DC V_{CC}/ground current ±50 mΑ Icc 300 (SM8) Power dissipation P_D mW 200 (US8) Storage temperature -65~150 °C T_{stg} 260 °C Lead temperature (10s) TL

Symbol

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test

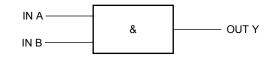
("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

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Truth Table

А	В	Y
L	L	L
L	Н	L
Н	L	L
Н	Н	Н





Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.65~5.5	V	
Supply vollage	VCC	1.5~5.5 (Note 1)	v	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	Vout	0~5.5 (Note 2)	v	
Output voltage		0~V _{CC} (Note 3)		
Operating temperature	T _{opr}	-40~85	°C	
		0~20 (V_{CC} = 1.8 V \pm 0.15 V, 2.5 V \pm 0.2 V)	ns/V	
Input rise and fall time	d _t /d _v	0~10 (V_{CC} = 3.3 V \pm 0.3 V)		
		0~5 (V_{CC} = 5.5 V \pm 0.5 V)		

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$

Note 3: High or low state

Electrical Characteristics

DC Characteristics

Characteristics		Symbol Test Condition		Condition		Ta = 25°C			Ta = -40~85°C		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
Input voltage	Lligh lovel	VIH	_		1.65~1.95	$0.75 \times V_{CC}$	_	_	$0.75 \times V_{CC}$	_	V
	riigirievei				2.3~5.5	$0.7 \\ \times V_{CC}$	_	_	$0.7 \times V_{CC}$	_	
	Low level	V.	/IL —		1.65~1.95			$\begin{array}{c} 0.25 \\ \times V_{CC} \end{array}$	_	$\begin{array}{c} 0.25 \\ \times V_{CC} \end{array}$	
		۷IL			2.3~5.5			$0.3 \\ \times V_{CC}$	_	$0.3 \\ \times V_{CC}$	
					1.65	1.55	1.65	—	1.55	_	
				I _{OH} = −100 μA	2.3	2.2	2.3	—	2.2	_	
				10H = -100 μΛ	3.0	2.9	3.0		2.9	—	
		V _{OH}	V _{IN} = V _{IH}		4.5	4.4	4.5		4.4	—	
	High level			I _{OH} = -4 mA	1.65	1.29	1.52	—	1.29	_	
				I _{OH} = -8 mA	2.3	1.9	2.15	—	1.9	_	
				I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	—	
				I _{OH} = -24 mA	3.0	2.3	2.68	—	2.3	—	
Output				I _{OH} = -32 mA	4.5	3.8	4.2	_	3.8	—	
voltage	Low level	V _{OL}	VIN = VIH or VIL	I _{OL} = 100 μA	1.65	—	0	0.1	—	0.1	v
					2.3	—	0	0.1		0.1	
					3.0	—	0	0.1		0.1	
					4.5	—	0	0.1		0.1	
				$I_{OL} = 4 \text{ mA}$	1.65	—	0.08	0.24		0.24	
				I _{OL} = 8 mA	2.3	—	0.1	0.3		0.3	
				I _{OL} = 16 mA	3.0	—	0.15	0.4		0.4	
				I _{OL} = 24 mA	3.0	—	0.22	0.55		0.55	
				$I_{OL} = 32 \text{ mA}$	4.5	_	0.22	0.55		0.55	
Input leakage	Input leakage current I_{IN} $V_{IN} = 5.5$ V or GND		0~5.5	—	_	±1		±10	μA		
Power off lea	kage current	IOFF	V_{IN} or $V_{OUT} = 5.5 V$		0.0	—	_	1		10	μA
Quiescent supply current		ICC	$V_{IN} = 5.5 \text{ V or GND}$		1.65~5.5	—	—	1		10	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH t _{pHL}	$C_L = 15 \text{ pF}, \text{ R}_L = 1 \text{ M}\Omega$	1.8 ± 0.15	2.0	5.7	10.5	2.0	11.0	• ns
			$\textbf{2.5}\pm\textbf{0.2}$	1.0	3.5	5.8	1.0	6.2	
			$\textbf{3.3}\pm\textbf{0.3}$	0.8	2.6	3.9	0.8	4.3	
			5.0 ± 0.5	0.5	1.9	3.1	0.5	3.3	
		$C_L = 50 \text{ pF}, \text{ R}_L = 500 \Omega$	$\textbf{3.3}\pm\textbf{0.3}$	1.2	3.2	4.8	1.2	5.2	
			5.0 ± 0.5	0.8	2.5	3.7	0.8	4.0	
Input capacitance	C _{IN}	—	0~5.5	_	3.0		_	_	pF
Power dissipation capacitance	CPD (Note 4)		3.3		22		_		
		5.5		37				рF	

Note 4:C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

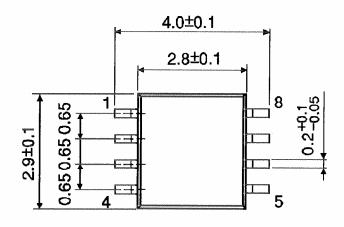
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

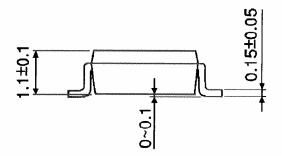
TOSHIBA

Package Dimensions

SSOP8-P-0.65

Unit : mm





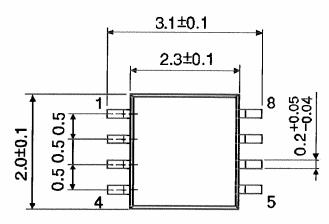
Weight: 0.02 g (typ.)

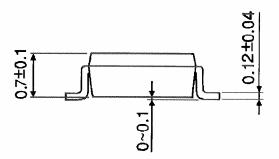
TOSHIBA

Package Dimensions

SSOP8-P-0.50A

Unit : mm





Weight: 0.01 g (typ.)

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20070701-EN GENERAL

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