

## P-Channel 30-V (D-S) MOSFET

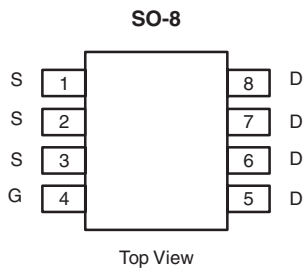
PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
- 30	0.014 at $V_{GS} = - 10$ V	- 11.5
	0.022 at $V_{GS} = - 4.5$ V	- 9.2

### FEATURES

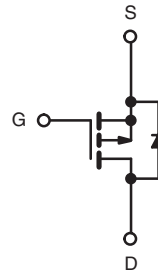
- TrenchFET<sup>®</sup> Power MOSFETs
- 100 %  $R_g$  Tested



**RoHS\***  
COMPLIANT



**Ordering Information:** Si4825DY-T1  
Si4825DY-T1-E3 (Lead (Pb)-free)



P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25$ °C, unless otherwise noted					
Parameter	Symbol	10 sec	Steady State	Unit	
Drain-Source Voltage	$V_{DS}$	- 30		V	
Gate-Source Voltage	$V_{GS}$	$\pm 25$			
Continuous Drain Current ( $T_J = 150$ °C) <sup>a</sup>	$I_D$	$T_A = 25$ °C	- 11.5	- 8.1	A
		$T_A = 70$ °C	- 9.2	- 6.5	
Pulsed Drain Current	$I_{DM}$	- 50			
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	- 2.5	- 1.3		
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25$ °C	3.0	1.5	W
		$T_A = 70$ °C	1.9	0.9	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150		°C	

<b>THERMAL RESISTANCE RATINGS</b>					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 10$ sec	$R_{thJA}$	32	42	°C/W
	Steady State		68	85	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	15	18	

Notes:

a. Surface Mounted on 1" x 1" FR4 Board.

\* Pb containing terminations are not RoHS compliant, exemptions may apply.

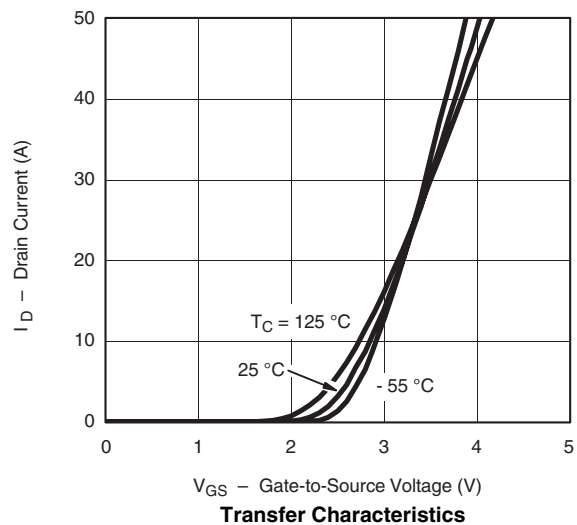
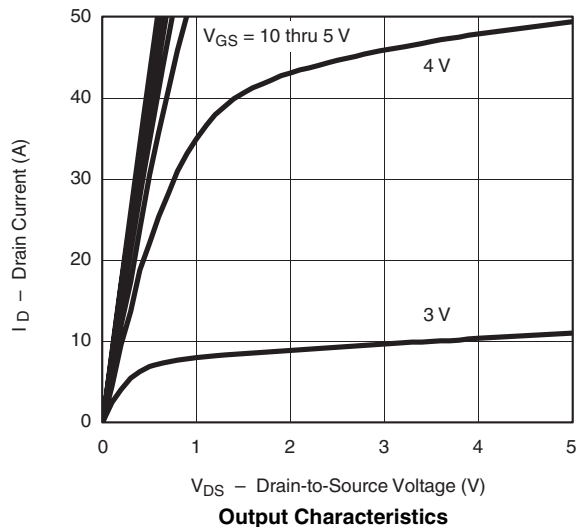
<b>SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-1.0		-3.0	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 25\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -30\text{ V}, V_{GS} = 0\text{ V}$			-1	$\mu\text{A}$
		$V_{DS} = -30\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			-5	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \leq -5\text{ V}, V_{GS} = -10\text{ V}$	-50			A
Drain-Source On-State Resistance <sup>a</sup>	$r_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -11.5\text{ A}$		0.012	0.014	$\Omega$
		$V_{GS} = -4.5\text{ V}, I_D = -9.2\text{ A}$		0.018	0.022	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -15\text{ V}, I_D = -11.5\text{ A}$		28		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -2.5\text{ A}, V_{GS} = 0\text{ V}$		-0.8	-1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = -15\text{ V}, V_{GS} = -10\text{ V}, I_D = -11.5\text{ A}$		55	71	nC
Gate-Source Charge	$Q_{gs}$			15.5		
Gate-Drain Charge	$Q_{gd}$			7.5		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		3.5	5.3	$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -15\text{ V}, R_L = 15\text{ }\Omega$ $I_D = -1\text{ A}, V_{GEN} = -10\text{ V}, R_g = 6\text{ }\Omega$		15	25	ns
Rise Time	$t_r$			13	20	
Turn-Off Delay Time	$t_{d(off)}$			97	150	
Fall Time	$t_f$			51	75	
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = -2.5\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		45	80	

Notes:

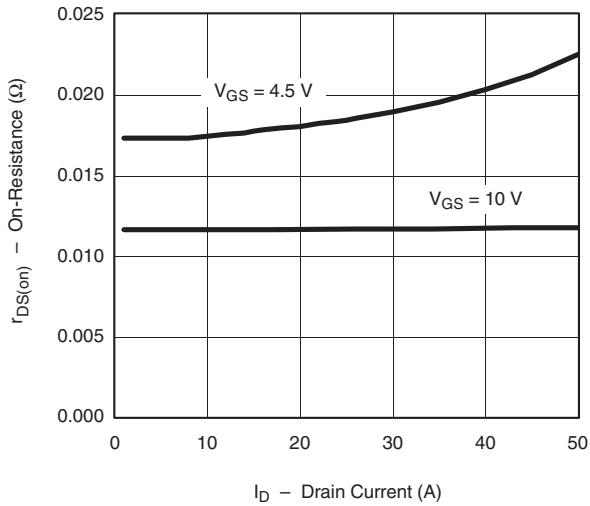
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

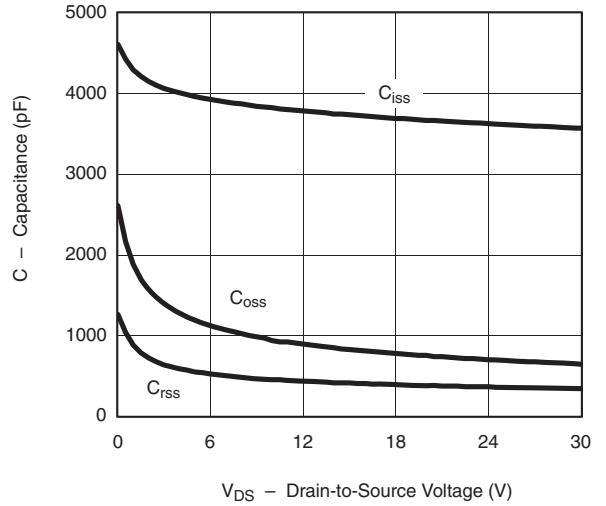
## TYPICAL CHARACTERISTICS $25\text{ }^\circ\text{C}$ , unless otherwise noted



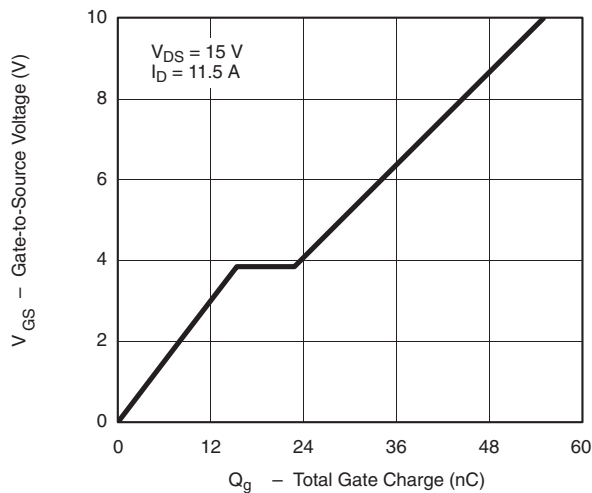
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



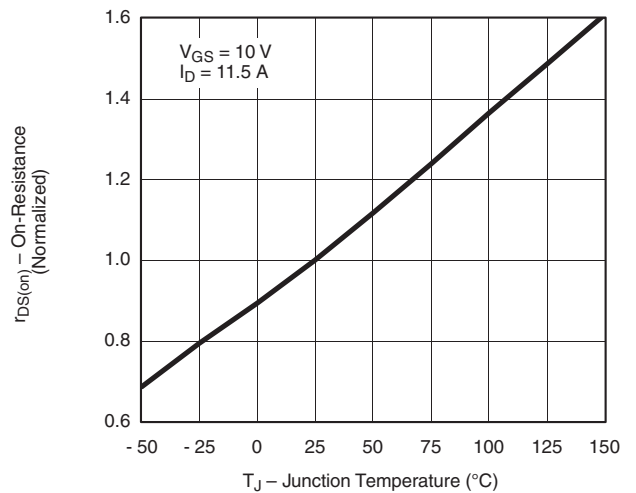
**On-Resistance vs. Drain Current**



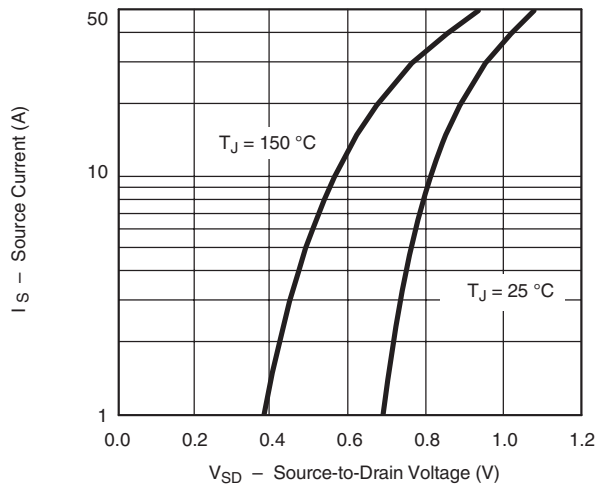
**Capacitance**



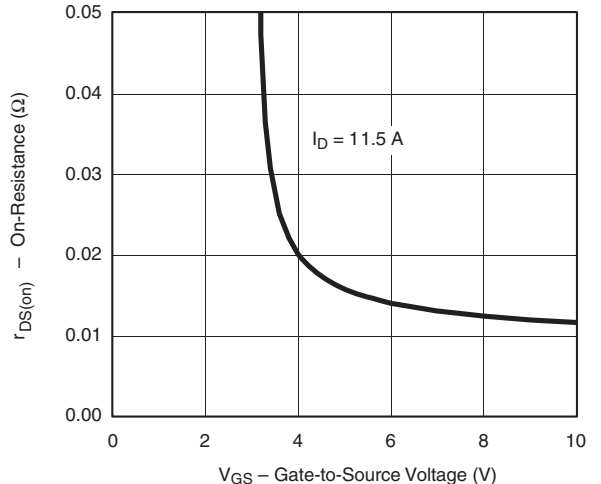
**Gate Charge**



**On-Resistance vs. Junction Temperature**

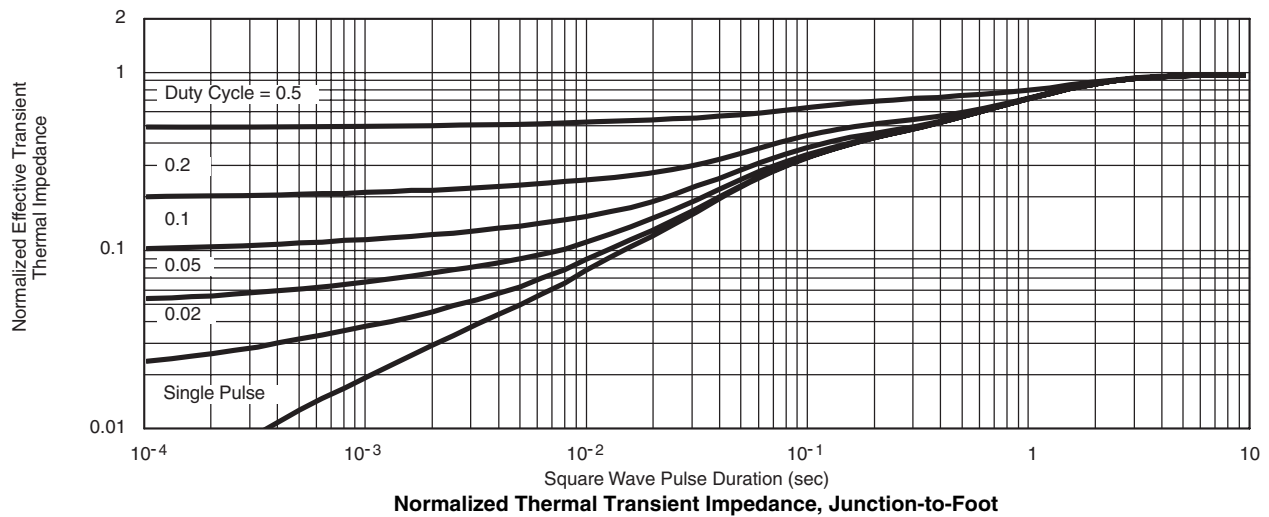
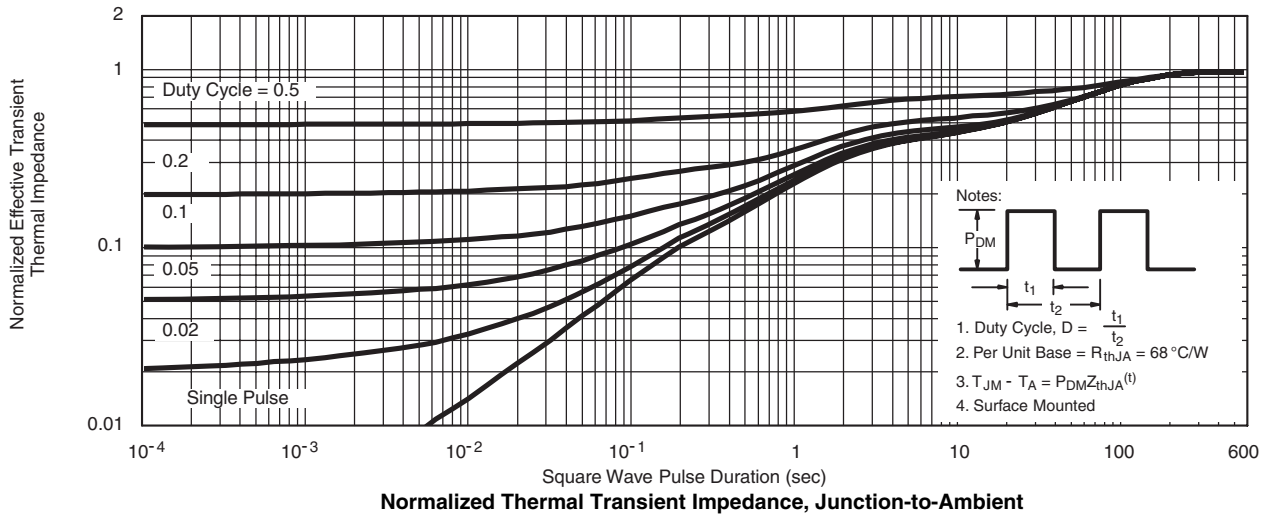
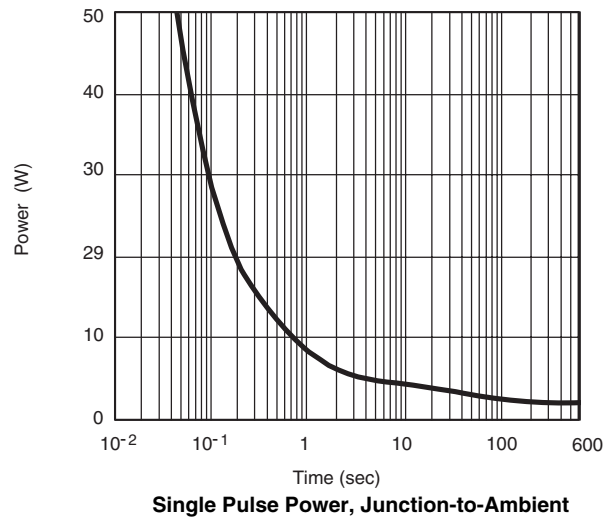
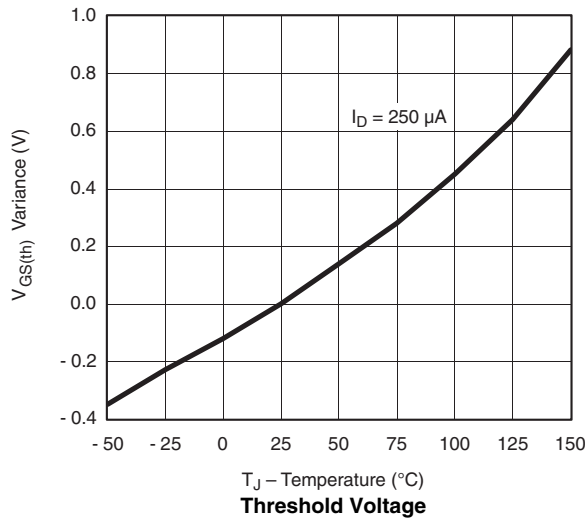


**Source-Drain Diode Forward Voltage**



**On-Resistance vs. Gate-to-Source Voltage**

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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